ALU control:

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Inputs | | | | Outputs | | | | | | |
| Instruction Type | OP code | FUN code | Instruction | ALUOP | | | | | STLU | Pass by |
|  |  |  |  | Binary | | | | HEX |  |  |
| R | 0 | 0 | ADD | 0 | 0 | 0 | 0 | 0 | X | 0 |
| I | 4 | X | ADDI | 0 | 0 | 0 | 0 | 0 | X | 0 |
| I | 16 | X | LW | 0 | 0 | 0 | 0 | 0 | X | 0 |
| I | 17 | X | SW | 0 | 0 | 0 | 0 | 0 | X | 0 |
| J | 22 | X | JALR | 0 | 0 | 0 | 0 | 0 | X | 1 |
| R | 0 | 1 | SUB | 0 | 0 | 0 | 1 | 1 | X | 0 |
| I | 18 | X | BEQ | 0 | 0 | 0 | 1 | 1 | X | 0 |
| I | 19 | X | BNE | 0 | 0 | 0 | 1 | 1 | X | 0 |
| R | 1 | 0 | XOR | 0 | 1 | 0 | 0 | 4 | X | 0 |
| I | 8 | X | XORI | 0 | 1 | 0 | 0 | 4 | X | 0 |
| R | 1 | 1 | OR | 0 | 1 | 0 | 1 | 5 | X | 0 |
| I | 9 | X | ORI | 0 | 1 | 0 | 1 | 5 | X | 0 |
| R | 1 | 2 | AND | 0 | 1 | 1 | 0 | 6 | X | 0 |
| I | 10 | X | ANDI | 0 | 1 | 1 | 0 | 6 | X | 0 |
| R | 1 | 3 | NOR | 0 | 1 | 1 | 1 | 7 | X | 0 |
| I | 11 | X | NORI | 0 | 1 | 1 | 1 | 7 | X | 0 |
| R | 2 | 0 | SLL | 1 | 0 | 0 | 0 | 8 | X | 0 |
| I | 12 | X | SLLI | 1 | 0 | 0 | 0 | 8 | X | 0 |
| R | 2 | 1 | SRL | 1 | 0 | 0 | 1 | 9 | X | 0 |
| I | 13 | X | SRLI | 1 | 0 | 0 | 1 | 9 | X | 0 |
| R | 2 | 2 | SRA | 1 | 0 | 1 | 0 | 10 | X | 0 |
| I | 14 | X | SRAI | 1 | 0 | 1 | 0 | 10 | X | 0 |
| R | 2 | 3 | ROR | 1 | 0 | 1 | 1 | 11 | X | 0 |
| I | 15 | X | RORI | 1 | 0 | 1 | 1 | 11 | X | 0 |
| R | 0 | 2 | SLT | 1 | 1 | 0 | 1 | 12 | 0 | 0 |
| I | 6 | X | SLTI | 1 | 1 | 0 | 1 | 12 | 0 | 0 |
| I | 20 | X | BLT | 1 | 1 | 0 | 1 | 12 | X | 0 |
| I | 21 | X | BGE | 1 | 1 | 0 | 1 | 12 | X | 0 |
| R | 0 | 3 | SLTU | 1 | 1 | 0 | 1 | 12 | 1 | 0 |
| I | 7 | X | SLTIU | 1 | 1 | 0 | 1 | 12 | 1 | 0 |
| J | 23 | X | LUI | X | X | X | X | 15 | X | 2 |
| J | 24 | X | J | X | X | X | X | 15 | X | 0 |
| J | 25 | X | JAL | X | X | X | X | 15 | X | 1 |
| X | 5 | X | Bubble | X | X | X | X | 15 | X | 3 |

Main control:

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Inputs | | Outputs | | | | | | | | | | | | |
| OP Code | Instruction | RegDst | RegW | Ext | ALUSRC | MEMR | MEMW | WBData | BEQ | BNE | BLT | BGE | Jump | Imm11 |
| 0-2 | R-Type | 1 | 1 | X | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 4 | ADDI | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 6 | SLTI | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 7 | SLTIU | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 8 | XORI | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 9 | ORI | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 10 | ANDI | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 11 | NORI | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 12 | SLLI | 0 | 1 | X | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 13 | SRLI | 0 | 1 | X | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 14 | SRAI | 0 | 1 | X | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 15 | RORI | 0 | 1 | X | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 16 | LW | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| 17 | SW | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 18 | BEQ | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| 19 | BNE | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| 20 | BLT | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| 21 | BGE | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| 22 | JALR | 0 | 1 | 1 | X | 0 | 0 | 3 | 0 | 0 | 0 | 0 | 1 | 0 |
| 23 | LUI | 2 | 1 | X | X | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 0 |
| 24 | J | X | 0 | X | X | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 |
| 25 | JAL | 3 | 1 | X | X | 0 | 0 | 3 | 0 | 0 | 0 | 0 | 1 | 1 |